Fifth International Workshop on Verification and Evaluation of Computer and Communication Systems (VECoS 2011)

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Editors

Nihal Perkegin

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Abstract

The aim of the VECoS workshop is to bring together researchers and practitioners in the areas of verification, control, performance, quality of service, dependability evaluation and assessment, to discuss the state of the art for solving the challenges facing us today in various modern computer and communication systems in which functional and extra-functional properties are strongly interrelated.

Thus, the main motivation for VECoS is to encourage the cross-fertilisation between formal verification and evaluation approaches, methods and techniques, especially those based on the specification formalisms for concurrent, distributed and soft/hard systems. Beyond its technical and scientific goals, another main purpose of VECoS is to promote collaboration between participants in research and education in the area of computer science and engineering.

This conference has been organised in partnership with the Tunis Science City.

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Papers:

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Imed Nasri, Georges Habchi and Reda Boukezzoula Scheduling and Control Modeling of HVLV Systems Using Max-Plus Algebra <http://dx.doi.org/10.14236/ewic/VECOS2011.5>

Mohammed Amin Ben Amar, Hervé Camus and Ouajdi Korbara A Mathematical Model for Cyclic Scheduling with Limited Work-In-Process and Cycle Time Minimization
<http://dx.doi.org/10.14236/ewic/VECOS2011.6>

Achraf Ben Said, Nejib Ben Hadj-Alouane, Moez Yeddes, Feng Lin An Iterative Approach for the Satisfaction of Security Using the Intransitive Non-Interference Property
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<http://dx.doi.org/10.14236/ewic/VECOS2011.9>

Yannick L Kala Konga, Karim Djouani and Guillaume Noel Modelling and Verification of JXTA peer-to-peer Network Protocols <http://dx.doi.org/10.14236/ewic/VECOS2011.10>

Nesrine Harrath, Bruno Monsuez and Joëlle Delacroix Building SystemC waiting state automata <http://dx.doi.org/10.14236/ewic/VECOS2011.11>

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Meriem Belguidoum, Faiza Belala and Fateh Latreche Toward a rewriting logic framework for safe and distributed component installation <http://dx.doi.org/10.14236/ewic/VECOS2011.15>